

Electronic Acknowledgement Receipt

EFS ID:	1056384
Application Number:	10070092
Confirmation Number:	7323
Title of Invention:	Local register instruction for selectively loading a register in a multithreaded parallel processor architecture
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Filer Authorized By:	Ido Rabinovitch
Attorney Docket Number:	10559-309US1
Receipt Date:	24-MAY-2006
Filing Date:	28-JUN-2002
Time Stamp:	12:05:53
Application Type:	U.S. National Stage under 35 USC 371
International Application Number:	

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1		10559309US1Response.pdf	110012	yes	11
	Multipart Description				
	Doc Desc		Start	End	
	Amendment After Final		1	1	
	Amendment Copy Claims/Response to Suggested Claims		2	6	
	Applicant Arguments/Remarks Made in an Amendment		7	11	
Warnings:					
Information:					
Total Files Size (in bytes):			110012		
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